

FIG. 1

W.D.G. = Write Device Gate

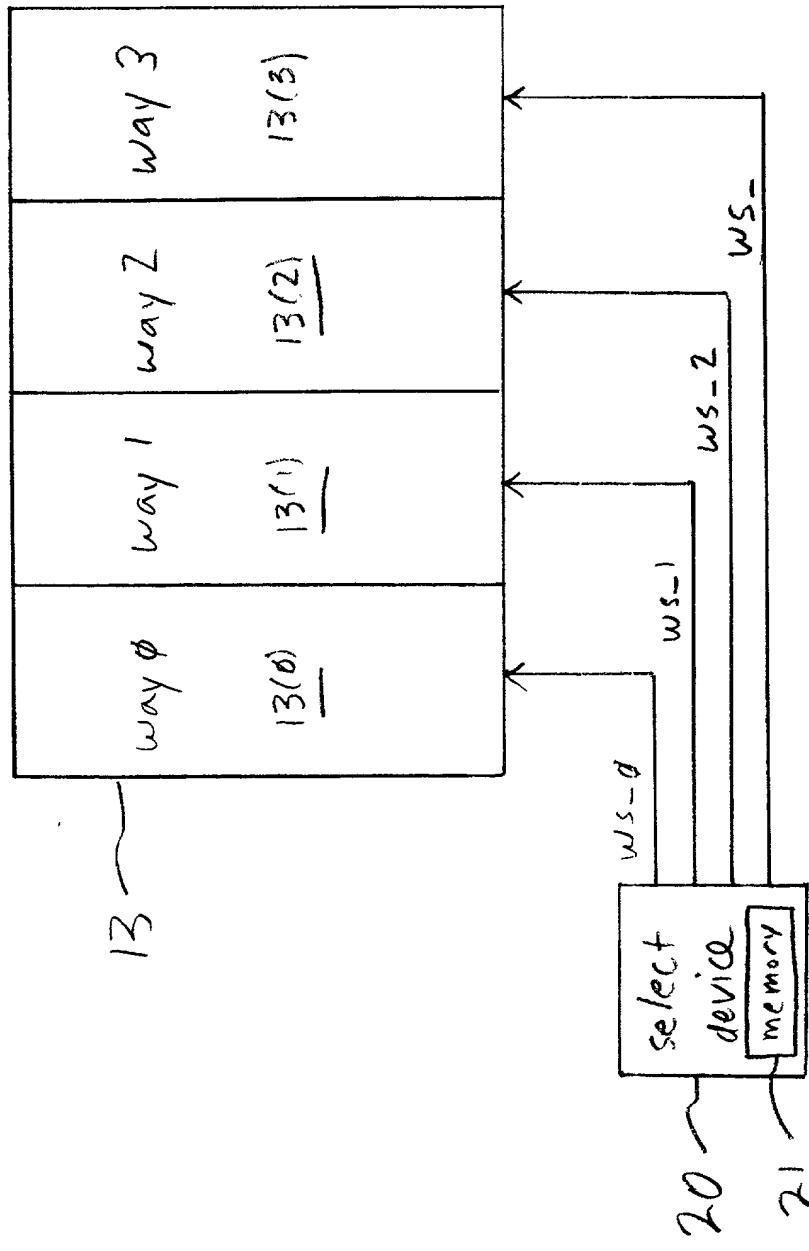


FIG. 2

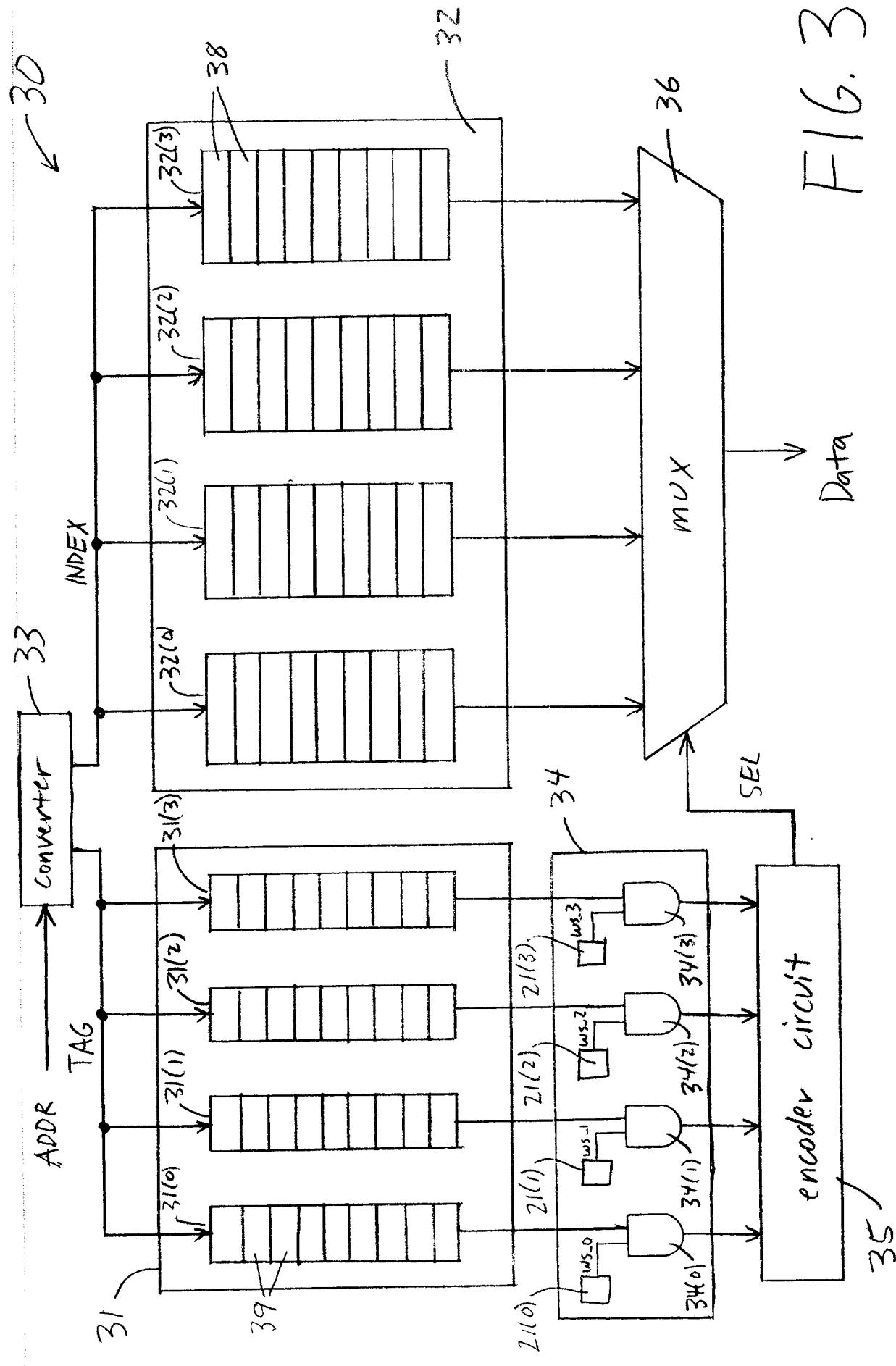


FIG. 3

Data

encoder circuit

35